

IN THE CLAIMS:

1. (Original) A communication receiver apparatus, comprising:
 - an input for receiving from a communication transmitter apparatus an input analog communication signal;
 - a feedforward equalizer coupled to said input for producing in response to said input analog communication signal an equalized analog communication signal;
 - a sampler coupled to said feedforward equalizer for producing digital communication information in response to said equalized analog communication signal; and
 - a feedback equalizer coupled between said sampler and said feedforward equalizer for controlling said feedforward equalizer in response to said digital communication information.
2. (Original) The apparatus of Claim 1, wherein said feedforward equalizer includes a wire summation node.
3. (Original) The apparatus of Claim 1, wherein said feedback equalizer includes a digital-to-analog conversion portion having an input coupled to said sampler for receiving said digital communication information, said digital-to-analog conversion portion having an output coupled to said feedforward equalizer.
4. (Original) The apparatus of Claim 3, wherein said feedforward equalizer includes a wire summation node.
5. (Original) The apparatus of Claim 3, wherein said digital-to-analog conversion portion includes a plurality of digital-to-analog converters having respective

inputs coupled to said sampler and having respective outputs coupled to said feedforward equalizer.

6. (Original) The apparatus of Claim 5, wherein each of said digital-to-analog converters includes a current source digital-to-analog converter.

7. (Original) The apparatus of Claim 6, wherein said outputs of said digital-to-analog converters are connected together at an input of said feedforward equalizer.

8. (Original) The apparatus of Claim 5, wherein said feedforward equalizer includes a wire summation node.

9. (Original) The apparatus of Claim 5, wherein said feedback equalizer includes a delay apparatus coupled between said sampler and said digital-to-analog converters for providing said digital communication information to said digital-to-analog converters at respectively different points in time.

10. (Original) The apparatus of Claim 1, wherein said input analog communication signal carries a SONET communication.

11. (Currently amended) The apparatus of Claim 1, wherein said feedback equalizer includes a control input for receiving first control information, said feedback equalizer responsive to said control information for controlling said feedforward equalizer, said control information designed to minimize interference at temporal boundaries between data symbols carried by said equalized analog communication signal.

12. (Currently amended) The apparatus of Claim 11, wherein said input analog communication signal is produced by the communication transmitter apparatus in response to [[further]] second control information, said [[first-mentioned]] first control information designed in conjunction with the [[further]] second control information to minimize interference at points in time between said temporal boundaries.

13. (Original) A communication transmitter apparatus, comprising:

- an input for receiving digital communication information;
- a digital-to-analog conversion portion coupled to said input for producing an analog communication signal in response to said digital communication information;
- an output coupled to said digital-to-analog conversion portion for providing said analog communication signal for transmission to a communication receiver apparatus;
- said digital-to-analog conversion portion having a control input for receiving control information, said digital-to-analog conversion portion for producing said analog communication signal in response to said control information, said control information defined based on feedback coefficients used by a decision feedback equalizer in the communication receiver apparatus.

14. (Original) The apparatus of Claim 13, wherein said digital-to-analog conversion portion includes a plurality of current source digital-to-analog converters, and wherein said control information includes weight information for indicating respective amounts of current to be sourced by said current source digital-to-analog converters.

15. (Original) The apparatus of Claim 13, wherein said control information is defined in conjunction with the feedback coefficients to minimize interference at points in time between temporal boundaries of data symbols carried by an equalized communication signal produced by the decision feedback equalizer.

16. (Original) The apparatus of Claim 15, wherein the feedback coefficients are defined in conjunction with said control information to minimize interference at said temporal boundaries.

17. (Original) A decision feedback equalizer apparatus, comprising:

- an input for receiving an input communication signal;
- an output for providing an equalized communication signal;
- an equalizer coupled between said input and said output for providing said equalized communication signal in response to said input communication signal, said equalizer having a control input for receiving an equalizer coefficient, said equalizer further responsive to said equalizer coefficient for producing said equalized communication signal; and
- a coefficient adaptor apparatus coupled to said equalizer for producing said equalizer coefficient, said coefficient adaptor apparatus having an input for receiving information indicative of a temporal relationship between first and second points in time, said first point in time corresponding to an actual occurrence of a temporal boundary between data symbols carried by said equalized communication signal, said second point in time corresponding to an expected occurrence of said temporal boundary, and said coefficient adaptor apparatus for iteratively adapting said equalizer coefficient in response to said temporal relationship information.

18. (Original) The apparatus of Claim 17, wherein said temporal relationship information indicates when said first point in time precedes said second point in time, and also indicates when said second point in time precedes said first point in time.

19. (Currently amended) The apparatus of Claim 17, wherein said coefficient adaptor apparatus includes logic for producing, in response to said temporal relationship information, [[further]] equalizer information indicative of how said equalized communication signal is affected by a current version of said equalizer coefficient.

20. (Currently amended) The apparatus of Claim 19, wherein said coefficient adaptor apparatus includes a coefficient adaptor coupled to said logic and having an input for receiving said current version of said equalizer coefficient, said coefficient adaptor responsive to said [[further]] equalizer information for adapting said current version of said equalizer coefficient to produce a corresponding adapted version of said equalizer coefficient.

21. (Original) The apparatus of Claim 19, wherein said logic includes a lookup table.

22. (Original) The apparatus of Claim 17, wherein said coefficient adaptor apparatus is for implementing a LMS algorithm to iteratively adapt said equalizer coefficient.

23. (Original) A method of communication reception, comprising:
feedforward equalizing an input analog communication signal to produce an equalized analog communication signal;
producing digital communication information in response to said equalized analog communication signal; and
performing said feedforward equalizing step based on said digital communication information.

24. (Original) The method of Claim 23, including converting said digital communication information into an analog control signal, and performing said feedforward equalization step in response to said analog control signal.

25. (Original) The method of Claim 24, wherein said analog control signal is a current signal.